



NOTES:

- 1) The simplified schematic for the original prototype can be found at <https://nodus.ligo.caltech.edu:8081/CTN/2491>.
- 2) The initial prototype was constructed on three boards (LIGO-D1900129-v2). The divisions of those boards are noted in the schematic.
- 3) Resistors noted as R_Test are reserved for 0Ω resistors that can be removed for testing.
- 4) When using the offset null on pins 1 and 8 of the opamp, connect a 0Ω resistor to the corresponding voltage desired.
- 5) Use fast and low noise opamps like AD829. Opamps like AD829 sometimes require a compensation capacitor placed on pin 5. If not using a compensation pin leave the capacitor pads attached to pin 5 unpopulated.
- 6) The capacitor labeled C_OPT1 should remain unpopulated unless needed.

Ian A. O. MacMillan macmillan@caltech.edu		Caltech CTN Lab	
TITLE: FSS Plant Circuit			
Document Number:	LIGO-D2000020-v1		REV:
Date: 1/22/20 8:31 PM	Sheet: 1/1		