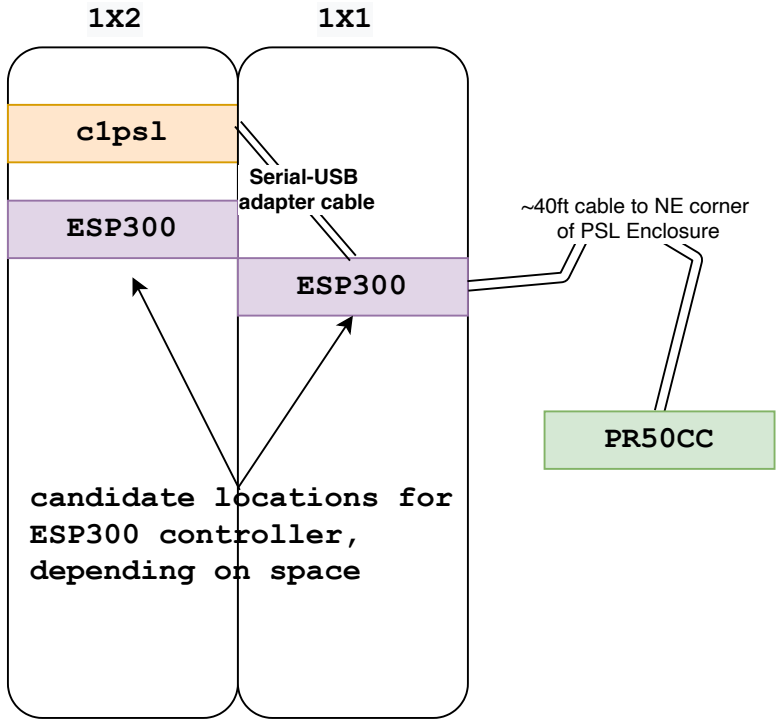


## Proposed hardware layout



## Proposed software logic

