

c1psl_feedthrough_wiring

EPICS Channel Name	Channel Type	Feedthrough	Signal	Return	Acromag	Signal	Return	Bench Tested	In Situ Tested
DB37M-1 (to PSL TTFSS, P1-1)									
C1:PSL-FSS_SW1	Digital Out	DB37M-1	1	20	BIO0	7	RTN	OK	OK
C1:PSL-FSS_SW2	Digital Out	DB37M-1	2	21	BIO0	8	RTN	OK	OK
C1:PSL-FSS_INOFFSET	Analog Out	DB37M-1	3	22	AO0	2	RTN	OK	OK
C1:PSL-FSS_MGAIN	Analog Out	DB37M-1	5	24	AO0	3	RTN	OK	OK
C1:PSL-FSS_MIXERM	Analog In	DB37M-1	6	25	AI5	1+	1-	Added post installation	OK
C1:PSL-FSS_SLOWDC	Analog Out	DB37M-1	7	26	AO0	4	RTN	OK	OK
C1:PSL-FSS_SLOWM	Analog In	DB37M-1	8	27	AI5	6+	6-	OK	OK
C1:PSL-FSS_FASTGAIN	Analog Out	DB37M-1	11	30	AO0	1	RTN	OK	OK, check cal
C1:PSL-FSS_FAST	Analog In	DB37M-1	12	31	AI5	0+	0-	OK	OK, check cal
C1:PSL-FSS_PCDRIVE	Analog In	DB37M-1	13	32	AI5	2+	2-	OK	OK
C1:PSL-FSS_FASTSWEEP	Digital Out	DB37M-1	14	33	BIO0	6	RTN	OK	not tested
DB37M-2 (to PSL FREQ REF, P1-1)									
C1:PSL-PMC_PHCON	Analog Out	DB37M-2	1	20	AO1	2	RTN	OK	OK
C1:PSL-PMC_RFADJ	Analog Out	DB37M-2	2	21	AO1	4	RTN	OK	OK, check cal
C1:PSL-PMC_MODET	Analog In	DB37M-2	3	22	AI6	2+	2-	OK	OK, check cal
C1:PSL-PMC_PHFLIP	Digital Out	DB37M-2	4	23	BIO2	9	RTN	OK	OK
DB37M-3 (to PMC SERVO CARD, P1-1)									
C1:PSL-PMC_LODET	Analog In	DB37M-3	1	20	AI6	1+	1-	OK	N.C.
C1:PSL-PMC_PMCERR	Analog In	DB37M-3	2	21	AI6	3+	3-	OK	OK
C1:PSL-PMC_PZT	Analog In	DB37M-3	3	22	AI6	5+	5-	OK	OK
C1:PSL-PMC_GAIN	Analog Out	DB37M-3	4	23	AO1	0	RTN	OK	OK
C1:PSL-PMC_INOFFSET	Analog Out	DB37M-3	5	24	AO1	1	RTN	OK	OK
C1:PSL-PMC_SW1	Digital Out	DB37M-3	6	25	BIO2	10	RTN	OK	OK
C1:PSL-PMC_SW2	Digital Out	DB37M-3	7	26	BIO2	11	RTN	OK	OK
C1:PSL-PMC_RAMP	Analog Out	DB37M-3	8	27	AO1	3	RTN	OK	OK
C1:PSL-PMC_BLANK	Digital Out	DB37M-3	9	28	BIO2	8	RTN	OK	OK
DB37M-4 (to PDs directly)									
C1:PSL-FSS_RFPDDC	Analog In	DB37M-4	9	28	AI5	5+	5-	OK	N.C.
C1:PSL-FSS_RCTRANSPD	Analog In	DB37M-4	10	29	AI5	4+	4-	OK	N.C.
C1:PSL-PMC_RFPDDC	Analog In	DB37M-4	11	30	AI6	6+	6-	OK	OK
DB37M-5 (to MC WFS1 DEMOD, P1-1)									
C1:IOO-WFS1_SEG1_ATTEN	Digital Out	DB37M-5	1	20	BIO2	0	RTN	OK	OK
C1:IOO-WFS1_SEG2_ATTEN	Digital Out	DB37M-5	2	21	BIO2	1	RTN	OK	OK
C1:IOO-WFS1_SEG3_ATTEN	Digital Out	DB37M-5	3	22	BIO2	2	RTN	OK	OK
C1:IOO-WFS1_SEG4_ATTEN	Digital Out	DB37M-5	4	23	BIO2	3	RTN	OK	OK
C1:IOO-WFS1_LO_LOCK_MON	Analog In	DB37M-5	10	29	AI2	0+	0-	OK	OK
C1:IOO-WFS1_SEG1_I	Analog In	DB37M-5	11	30	AI1	0+	0-	OK	OK
C1:IOO-WFS1_SEG2_I	Analog In	DB37M-5	12	31	AI1	2+	2-	OK	OK
C1:IOO-WFS1_SEG3_I	Analog In	DB37M-5	13	32	AI1	4+	4-	OK	OK
C1:IOO-WFS1_SEG4_I	Analog In	DB37M-5	14	33	AI1	6+	6-	OK	OK
C1:IOO-WFS1_SEG1_Q	Analog In	DB37M-5	15	34	AI1	1+	1-	OK	OK
C1:IOO-WFS1_SEG2_Q	Analog In	DB37M-5	16	35	AI1	3+	3-	OK	OK
DB37M-6 (to MC WFS1 DEMOD, P1-2)									
C1:IOO-WFS1_SEG3_Q	Analog In	DB37M-6	1	20	AI1	5+	5-	OK	OK
C1:IOO-WFS1_SEG4_Q	Analog In	DB37M-6	2	21	AI1	7+	7-	OK	OK
C1:IOO-WFS1_SEG1_DC	Analog In	DB37M-6	3	22	AI2	1+	1-	OK	OK
C1:IOO-WFS1_SEG2_DC	Analog In	DB37M-6	4	23	AI2	2+	2-	OK	OK
C1:IOO-WFS1_SEG3_DC	Analog In	DB37M-6	5	24	AI2	3+	3-	OK	OK
C1:IOO-WFS1_SEG4_DC	Analog In	DB37M-6	6	25	AI2	4+	4-	OK	OK
DB37M-7 (to MC WFS2 DEMOD, P1-1)									
C1:IOO-WFS2_SEG1_ATTEN	Digital Out	DB37M-7	1	20	BIO2	4	RTN	OK	OK
C1:IOO-WFS2_SEG2_ATTEN	Digital Out	DB37M-7	2	21	BIO2	5	RTN	OK	OK
C1:IOO-WFS2_SEG3_ATTEN	Digital Out	DB37M-7	3	22	BIO2	6	RTN	OK	OK
C1:IOO-WFS2_SEG4_ATTEN	Digital Out	DB37M-7	4	23	BIO2	7	RTN	OK	OK
C1:IOO-WFS2_LO_LOCK_MON	Analog In	DB37M-7	10	29	AI4	0+	0-	OK	OK
C1:IOO-WFS2_SEG1_I	Analog In	DB37M-7	11	30	AI3	0+	0-	OK	OK

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C1:IOO-WFS2_SEG2_I	Analog In	DB37M-7	12	31	AI3	2+	2-	OK	OK
C1:IOO-WFS2_SEG3_I	Analog In	DB37M-7	13	32	AI3	4+	4-	OK	OK
C1:IOO-WFS2_SEG4_I	Analog In	DB37M-7	14	33	AI3	6+	6-	OK	OK
C1:IOO-WFS2_SEG1_Q	Analog In	DB37M-7	15	34	AI3	1+	1-	OK	OK
C1:IOO-WFS2_SEG2_Q	Analog In	DB37M-7	16	35	AI3	3+	3-	OK	OK
DB37M-8 (to MC WFS2 DEMOD, P1-2)									
C1:IOO-WFS2_SEG3_Q	Analog In	DB37M-8	1	20	AI3	5+	5-	OK	OK
C1:IOO-WFS2_SEG4_Q	Analog In	DB37M-8	2	21	AI3	7+	7-	OK	OK
C1:IOO-WFS2_SEG1_DC	Analog In	DB37M-8	3	22	AI4	1+	1-	OK	OK
C1:IOO-WFS2_SEG2_DC	Analog In	DB37M-8	4	23	AI4	2+	2-	OK	OK
C1:IOO-WFS2_SEG3_DC	Analog In	DB37M-8	5	24	AI4	3+	3-	OK	OK
C1:IOO-WFS2_SEG4_DC	Analog In	DB37M-8	6	25	AI4	4+	4-	OK	OK
DB37M-9 (to MC IQ DEMOD, P1-1)									
C1:IOO-MC_DEMOD_LO	Analog In	DB37M-9	3	22	AI0	0+	0-	OK	OK
DB37M-10 (to IMC SERVO, P1-1)									
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	1	20	BIO1	0	RTN	OK	GV to test
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	2	21	BIO1	1	RTN	OK	GV to test
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	3	22	BIO1	2	RTN	OK	GV to test
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	4	23	BIO1	3	RTN	OK	GV to test
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	5	24	BIO1	4	RTN	OK	GV to test
C1:IOO-MC_REFL_BITS	Digital Out	DB37M-10	6	25	BIO1	5	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	7	26	BIO1	6	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	8	27	BIO1	7	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	9	28	BIO1	8	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	10	29	BIO1	9	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	11	30	BIO1	10	RTN	OK	GV to test
C1:IOO-MC_AO_BITS	Digital Out	DB37M-10	12	31	BIO1	11	RTN	OK	GV to test
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-10	13	32	BIO0	0	RTN	OK	GV to test
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-10	14	33	BIO0	1	RTN	OK	GV to test
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-10	15	34	BIO0	2	RTN	OK	GV to test
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-10	16	35	BIO0	3	RTN	OK	GV to test
DB37M-11 (to IMC SERVO, P1-2)									
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-11	1	20	BIO0	4	RTN	OK	GV to test
C1:IOO-MC_VCO_BITS	Digital Out	DB37M-11	2	21	BIO0	5	RTN	OK	GV to test
C1:IOO-MC_SW1	Digital Out	DB37M-11	3	22	BIO3	0	RTN	OK	OK
C1:IOO-MC_SW2	Digital Out	DB37M-11	4	23	BIO3	1	RTN	OK	OK
C1:IOO-MC_SW3	Digital Out	DB37M-11	5	24	BIO3	2	RTN	OK	OK
C1:IOO-MC_BOOST2_BITS	Digital Out	DB37M-11	6	25	BIO3	3	RTN	OK	GV to test
C1:IOO-MC_BOOST2_BITS	Digital Out	DB37M-11	7	26	BIO3	4	RTN	OK	GV to test
C1:IOO-MC_BOOST1	Digital Out	DB37M-11	8	27	BIO3	5	RTN	OK	GV to test
C1:IOO-MC_EXCA_EN	Digital Out	DB37M-11	9	28	BIO3	6	RTN	OK	OK
C1:IOO-MC_OPTIONA	Digital Out	DB37M-11	10	29	BIO3	7	RTN	OK	OK
C1:IOO-MC_FILTER	Digital Out	DB37M-11	11	30	BIO3	8	RTN	OK	GV to test
C1:IOO-MC_FASTSW	Digital Out	DB37M-11	12	31	BIO3	9	RTN	OK	OK
C1:IOO-MC_POL	Digital Out	DB37M-11	13	32	BIO3	10	RTN	OK	OK
C1:IOO-MC_EXCB_EN	Digital Out	DB37M-11	14	33	BIO3	11	RTN	OK	OK
C1:IOO-MC_OPTIONB	Digital Out	DB37M-11	15	34	BIO3	12	RTN	OK	OK
C1:IOO-MC_LIMITER	Digital Out	DB37M-11	16	35	BIO3	13	RTN	OK	OK
DB15M-1 (to IMC SERVO, P2)									
C1:IOO-MC_SUM_MON	Analog In	DB15M-1	1	9	AI0	4+	4-	OK	OK
C1:IOO-MC_SLOW_MON	Analog In	DB15M-1	2	10	AI0	3+	3-	OK	OK
C1:IOO-MC_FAST_MON	Analog In	DB15M-1	3	11	AI0	1+	1-	OK	OK
C1:IOO-MC_LIMIT	Digital In	DB15M-1	4	12	BIO3	14	RTN	OK	OK
C1:IOO-MC_LATCH_EN	Digital Out	DB15M-1	5	13	BIO3	15	RTN	OK	OK
C1:IOO-MC_REFL_OFFSET	Analog Out	DB15M-1	6	14	AO0	0	RTN	OK	OK
DB37M-12 (to LSC PHOTODIODE INTERFACE, P1-1)									
C1:IOO-MC_RFPD_DCMON	Analog In	DB37M-12	3	22	AI0	2+	2-	OK	OK

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DB37M-13 (to PSL POS & ANG, P1-1)									
C1:PSL-QPD_ANG_HOR	Analog In	DB37M-13	5	24	AI7	0+	0-	OK	Board not working
C1:PSL-QPD_ANG_VERT	Analog In	DB37M-13	6	25	AI7	2+	2-	OK	Board not working
C1:PSL-QPD_ANG_SUM	Analog In	DB37M-13	7	26	AI7	1+	1-	OK	Board not working
C1:PSL-QPD_POS_HOR	Analog In	DB37M-13	12	31	AI7	3+	3-	OK	Board not working
C1:PSL-QPD_POS_VERT	Analog In	DB37M-13	13	32	AI7	5+	5-	OK	Board not working
C1:PSL-QPD_POS_SUM	Analog In	DB37M-13	14	33	AI7	4+	4-	OK	Board not working
DB37M-14 (spare AI channels)									
OPEN	Analog In	DB37M-14	1	20	AI4	5+	5-		
OPEN	Analog In	DB37M-14	2	21	AI4	6+	6-		
OPEN	Analog In	DB37M-14	3	22	AI4	7+	7-		
OPEN	Analog In	DB37M-14	4	23	AI7	6+	6-		
OPEN	Analog In	DB37M-14	5	24	AI7	7+	7-		
OPEN	Analog In	DB37M-14	6	25	AI8	0+	0-		
OPEN	Analog In	DB37M-14	7	26	AI8	1+	1-		
OPEN	Analog In	DB37M-14	8	27	AI8	2+	2-		
OPEN	Analog In	DB37M-14	9	28	AI8	3+	3-		
OPEN	Analog In	DB37M-14	10	29	AI8	4+	4-		
OPEN	Analog In	DB37M-14	11	30	AI8	5+	5-		
OPEN	Analog In	DB37M-14	12	31	AI8	6+	6-		
OPEN	Analog In	DB37M-14	13	32	AI8	7+	7-		
OPEN	Analog In	DB37M-14	14	33	AI6	7+	7-		
DB37M-15 (spare AO channels)									
OPEN	Analog Out	DB37M-15	1	20	AO0	5	RTN		
OPEN	Analog Out	DB37M-15	2	21	AO0	6	RTN		
OPEN	Analog Out	DB37M-15	3	22	AO0	7	RTN		
OPEN	Analog Out	DB37M-15	4	23	AO1	5	RTN		
OPEN	Analog Out	DB37M-15	5	24	AO1	6	RTN		
OPEN	Analog Out	DB37M-15	6	25	AO1	7	RTN		
OPEN	Analog Out	DB37M-15	7	26	AO2	0	RTN		
OPEN	Analog Out	DB37M-15	8	27	AO2	1	RTN		
OPEN	Analog Out	DB37M-15	9	28	AO2	2	RTN		
OPEN	Analog Out	DB37M-15	10	29	AO2	3	RTN		
OPEN	Analog Out	DB37M-15	11	30	AO2	4	RTN		
OPEN	Analog Out	DB37M-15	12	31	AO2	5	RTN		
OPEN	Analog Out	DB37M-15	13	32	AO2	6	RTN		
OPEN	Analog Out	DB37M-15	14	33	AO2	7	RTN		
DB37M-16 (spare SINKING BIO channels)									
OPEN	Digital I/O	DB37M-16	1	20	BIO0	6	RTN		
OPEN	Digital I/O	DB37M-16	2	21	BIO0	7	RTN		
OPEN	Digital I/O	DB37M-16	3	22	BIO0	8	RTN		
OPEN	Digital I/O	DB37M-16	4	23	BIO0	9	RTN		
OPEN	Digital I/O	DB37M-16	5	24	BIO0	10	RTN		
OPEN	Digital I/O	DB37M-16	6	25	BIO0	11	RTN		
OPEN	Digital I/O	DB37M-16	7	26	BIO0	12	RTN		
OPEN	Digital I/O	DB37M-16	8	27	BIO0	13	RTN		
OPEN	Digital I/O	DB37M-16	9	28	BIO0	14	RTN		
OPEN	Digital I/O	DB37M-16	10	29	BIO0	15	RTN		
OPEN	Digital I/O	DB37M-16	11	30	BIO1	12	RTN		
OPEN	Digital I/O	DB37M-16	12	31	BIO1	13	RTN		
OPEN	Digital I/O	DB37M-16	13	32	BIO1	14	RTN		
OPEN	Digital I/O	DB37M-16	14	33	BIO1	15	RTN		
OPEN	Digital I/O	DB37M-16	16	35	BIO2	13	RTN		
DB37M-17 (spare SOURCING BIO channels)									
OPEN	Digital I/O	DB37M-17	1	20	BIO4	0	RTN		
OPEN	Digital I/O	DB37M-17	2	21	BIO4	1	RTN		
OPEN	Digital I/O	DB37M-17	3	22	BIO4	2	RTN		
OPEN	Digital I/O	DB37M-17	4	23	BIO4	3	RTN		
OPEN	Digital I/O	DB37M-17	5	24	BIO4	4	RTN		
OPEN	Digital I/O	DB37M-17	6	25	BIO4	5	RTN		

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OPEN	Digital I/O	DB37M-17	7	26	BIO4	10	RTN		
OPEN	Digital I/O	DB37M-17	8	27	BIO4	11	RTN		
OPEN	Digital I/O	DB37M-17	9	28	BIO4	12	RTN		
OPEN	Digital I/O	DB37M-17	10	29	BIO4	13	RTN		
OPEN	Digital I/O	DB37M-17	11	30	BIO4	14	RTN		
OPEN	Digital I/O	DB37M-17	12	31	BIO4	15	RTN		
DB25M-1 (to NPRO DIAGNOSTICS)									
C1:PSL-NPRO_D1_POW	Analog In	DB25M-1	1	14	AI0	5+	5-	OK	OK
C1:PSL-NPRO_D2_POW	Analog In	DB25M-1	2	15	AI2	5+	5-	OK	OK
C1:PSL-NPRO_TEMP_ERR	Analog In	DB25M-1	3	16	AI6	0+	0-	OK	OK
C1:PSL-NPRO_D1_TERR	Analog In	DB25M-1	6	19	AI0	6+	6-	OK	OK
C1:PSL-NPRO_D2_TERR	Analog In	DB25M-1	7	20	AI2	6+	6-	OK	OK
C1:PSL-NPRO_D1_TGUARD	Analog In	DB25M-1	8	21	AI0	7+	7-	OK	OK
C1:PSL-NPRO_D2_TGUARD	Analog In	DB25M-1	9	22	AI2	7+	7-	OK	OK
C1:PSL-NPRO_INTERLOCK	Digital In	DB25M-1	13	23	BIO2	14	RTN	OK	OK
DB25M-2 (to TEMP SENSOR BOX)									
C1:PSL-FSS_RCTEMP	Analog In	DB25M-2	4	17	AI5	3+	3-	modified post installation	awaiting cable
C1:PSL-FSS_RMTEMP_VOLTS	Analog In	DB25M-2	5	18	AI5	7+	7-	modified post installation	awaiting cable
BNC CONNECTORS									
C1:AUX-PSL_Shutter	Digital Out	BNC-1	CENTER	SHIELD	BIO2	12	RTN	OK	OK
C1:PSL-PMC_PMCTRANSPD	Analog In	BNC-4	CENTER	SHIELD	AI6	4+	4-	OK	OK