

#	HOST	db file	channel name	type	description	module	channel	Note	Device on Eurocard	P1/P2	Pin Number	Totals	
1	c1psl	c1psl/psl.db	C1:PSL-FSS_RFPDDC	ai	RFPDDC- RFPD DC output	VMIVME-3113	#CO S21 @	FSS PD	Directly to the VME	P1		ai	57
2	c1psl	c1psl/psl.db	C1:PSL-FSS_LODET	ai	LODET- detected local oscillator level	VMIVME-3113	#CO S22 @	FSS Demod	TTFSS Interface (D04042 P1		4A	ao	13
3	c1psl	c1psl/psl.db	C1:PSL-FSS_FAST	ai	FAST- fast actuator input	VMIVME-3113	#CO S24 @	FSS	TTFSS Interface (D04042 P1		12A	bi	1
4	c1psl	c1psl/psl.db	C1:PSL-FSS_PCDRIVE	ai	PCDRIVE- reference cavity Pockels cell input	VMIVME-3113	#CO S25 @	FSS	TTFSS Interface (D04042 P1		13A	bo	36
5	c1psl	c1psl/psl.db	C1:PSL-FSS_RCTRANSPD	ai	RCTRANSPD- reference cavity transmitted light	VMIVME-3123	#CO S9 @	though it is not used	Directly to the VME				
6	c1psl	c1psl/psl.db	C1:PSL-PMC_RFPDDC	ai	RFPDDC- RFPD DC output	VMIVME-3113	#CO S32 @	PMC PD. It's either connected directly to the VME or through the DAQ interface					
7	c1psl	c1psl/psl.db	C1:PSL-PMC_LODET	ai	LODET- detected local oscillator level	VMIVME-3113	#CO S33 @	PMC Demod	PMC SERVO (D980352)	P1	1A		
8	c1psl	c1psl/psl.db	C1:PSL-PMC_PZT	ai	PZT- PMC PZT output	VMIVME-3113	#CO S36 @	PMC	PMC SERVO (D980352)	P1	3A		
9	c1psl	c1psl/psl.db	C1:PSL-PMC_MODET	ai	MODET- 35.5 MHz oscillator power	VMIVME-3113	#CO S38 @	PMC	Crystal Freq. Ref	P1	3A		
10	c1psl	c1psl/psl.db	C1:PSL-PPKTP_TEMP	ai	RMTEMP - room temperature	VMIVME-3123	#CO S7 @	Useful. But strange Ch Name.					
11	c1psl	c1psl/psl.db	C1:PSL-FSS_RCTEMP	ai	RCTEMP - reference cavity temperature	VMIVME-3123	#CO S8 @	Useful. But we should change the ch Name.					
12	c1psl	c1psl/psl.db	C1:PSL-FSS_SLOWM	ai	SLOWM- slow actuator voltage monitor	VMIVME-3123	#CO S1 @	Most Useful	TTFSS Interface (D04042 P1		8A		
13	c1psl	c1psl/psl.db	C1:PSL-PMC_PMCERR	ai	PMCERR- error point	VMIVME-3123	#CO S5 @	Useful	PMC SERVO (D980352)	P1	2A		
14	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_ANG_VERT	ai	IOO ANG QPD, vertical	VMIVME-3113	#C1 S5 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			6A/13A		
15	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_ANG_HOR	ai	IOO ANG QPD, horizontal	VMIVME-3113	#C1 S4 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			5A/12A		
16	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_ANG_SUM	ai	IOO ANG QPD, sum	VMIVME-3113	#C1 S6 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			7A/14A		
17	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_POS_HOR	ai	IOO POS QPD, horizontal	VMIVME-3113	#C1 S11 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			13A/6A		
18	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_POS_VERT	ai	IOO POS QPD, vertical	VMIVME-3113	#C1 S12 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			12A/5A		
19	c1ioo0	c1ioo0/ioo.db	C1:PSL-QPD_POS_SUM	ai	IOO POS QPD, sum	VMIVME-3113	#C1 S13 @	They are still on the PSL talPSL POS & ANG (D99069 P1/P2			14A/7A		
20	c1ioo0	c1ioo0/ioo.db	C1:PSL-PMC_INPUT_DC	ai	PMC Input DC Light Level	VMIVME-3113	#CO S60 @	Have never seen the PD. To be reviewed					
21	c1ioo0	c1ioo0/ioo.db	C1:IOO-MC_RFPD_DCMON	ai	MC RFPD DC Monitor Readback	VMIVME-3113	#CO S31 @	Heavily used	Probably connected directly to the VME				
22	c1ioo0	c1ioo0/ioo.db	C1:IOO-MC_DEMOD_LO	ai	I&R Demod Detected LO Readback	VMIVME-3113	#C1 S16 @	Occasionally useful if it is fr	mC IQ Demod (D990511 P1		3A		
23	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_LO_LOCK_MON	ai	Input Optics Lock Monitor from Demod1	VMIVME-3113	#CO S0 @409	Local Oscillator Lock Mon:	WFS Demod (D980233)	P1	10A		
24	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG1_I	ai	I1 Monitor from Demod1	VMIVME-3113	#CO S1 @	They are alive	WFS Demod (D980233)	P1	11A		
25	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG2_I	ai	I2 Monitor from Demod1	VMIVME-3113	#CO S2 @	They are alive	WFS Demod (D980233)	P1	12A		
26	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG3_I	ai	I3 Monitor from Demod1	VMIVME-3113	#CO S3 @	They are alive	WFS Demod (D980233)	P1	13A		
27	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG4_I	ai	I4 Monitor from Demod1	VMIVME-3113	#CO S4 @	They are alive	WFS Demod (D980233)	P1	14A		
28	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG1_Q	ai	Q1 Monitor from Demod1	VMIVME-3113	#CO S5 @	They are alive	WFS Demod (D980233)	P1	15A		
29	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG2_Q	ai	Q2 Monitor from Demod1	VMIVME-3113	#CO S6 @	They are alive	WFS Demod (D980233)	P1	16A		
30	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG3_Q	ai	Q3 Monitor from Demod1	VMIVME-3113	#CO S7 @	They are alive	WFS Demod (D980233)	P1	17A		
31	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG4_Q	ai	Q4 Monitor from Demod1	VMIVME-3113	#CO S8 @	They are alive	WFS Demod (D980233)	P1	18A		
32	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG1_DC	ai	D1 Monitor from Demod1	VMIVME-3113	#CO S9 @	They are alive	WFS Demod (D980233)	P1	19A		
33	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG2_DC	ai	D2 Monitor from Demod1	VMIVME-3113	#CO S10 @	They are alive	WFS Demod (D980233)	P1	20A		
34	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG3_DC	ai	D3 Monitor from Demod1	VMIVME-3113	#CO S11 @	They are alive	WFS Demod (D980233)	P1	21A		
35	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG4_DC	ai	D4 Monitor from Demod1	VMIVME-3113	#CO S12 @	They are alive	WFS Demod (D980233)	P1	22A		
36	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_LO_LOCK_MON	ai	Input Optics Lock Monitor from Demod2	VMIVME-3113	#CO S13 @40	Local Oscillator Lock Mon:	WFS Demod (D980233)	P1	10A		
37	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG1_I	ai	I1 Monitor from Demod2	VMIVME-3113	#CO S14 @	They are alive	WFS Demod (D980233)	P1	11A		
38	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG2_I	ai	I2 Monitor from Demod2	VMIVME-3113	#CO S15 @	They are alive	WFS Demod (D980233)	P1	12A		
39	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG3_I	ai	I3 Monitor from Demod2	VMIVME-3113	#CO S16 @	They are alive	WFS Demod (D980233)	P1	13A		
40	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG4_I	ai	I4 Monitor from Demod2	VMIVME-3113	#CO S17 @	They are alive	WFS Demod (D980233)	P1	14A		
41	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG1_Q	ai	Q1 Monitor from Demod2	VMIVME-3113	#CO S18 @	They are alive	WFS Demod (D980233)	P1	15A		
42	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG2_Q	ai	Q2 Monitor from Demod2	VMIVME-3113	#CO S19 @	They are alive	WFS Demod (D980233)	P1	16A		
43	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG3_Q	ai	Q3 Monitor from Demod2	VMIVME-3113	#CO S20 @	They are alive	WFS Demod (D980233)	P1	17A		
44	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG4_Q	ai	Q4 Monitor from Demod2	VMIVME-3113	#CO S21 @	They are alive	WFS Demod (D980233)	P1	18A		
45	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG1_DC	ai	D1 Monitor from Demod2	VMIVME-3113	#CO S22 @	They are alive	WFS Demod (D980233)	P1	19A		
46	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG2_DC	ai	D2 Monitor from Demod2	VMIVME-3113	#CO S23 @	They are alive	WFS Demod (D980233)	P1	20A		
47	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG3_DC	ai	D3 Monitor from Demod2	VMIVME-3113	#CO S24 @	They are alive	WFS Demod (D980233)	P1	21A		
48	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG4_DC	ai	D4 Monitor from Demod2	VMIVME-3113	#CO S25 @	They are alive	WFS Demod (D980233)	P1	22A		
49	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_PIT_OUT	ai	M1 Pit Mon	VMIVME-3113	#CO S42 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		5A		
50	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_YAW_OUT	ai	M1 Yaw+ Mon	VMIVME-3113	#CO S43 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		6A		
51	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_REF_HV	ai	M1 Yaw- Mon	VMIVME-3113	#CO S44 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		7A		
52	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_PIT_OUT	ai	M2 Pit Mon	VMIVME-3113	#CO S45 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		8A		
53	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_YAW_OUT	ai	M2 Yaw+ Mon	VMIVME-3113	#CO S46 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		9A		
54	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_REF_HV	ai	M2 Yaw- Mon	VMIVME-3113	#CO S47 @	Input steering. It's OK to le	Mode Cleaner Steering M P1		10A		
55	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_SUM_MON	ai	MC Sum Readback	VMIVME-3113	#CO S26 @	Useful.	MC Servo (D040180-B)				
56	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_SLOW_MON	ai	MC Slow Readback	VMIVME-3113	#CO S27 @	Useful. Length mon.	MC Servo (D040180-B)				
57	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_FAST_MON	ai	MC Fast Readback	VMIVME-3113	#CO S28 @	Useful.	MC Servo (D040180-B)				
58	c1psl	c1psl/psl.db	C1:PSL-FSS_INOFFSET	ao	INOFFSET- input offset trim	VMIVME-4116	#CO S5 @	Used everyday	TTFSS Interface (D04042 P1		3A		

59	c1psl	c1psl/psl.db	C1:PSL-FSS_MGAIN	ao	MGAIN- frequency stabilization servo loop gain	VMIVME-4116	#C0 S6 @	Used everyday	TTFSS Interface (D04042 P1	5A		
60	c1psl	c1psl/psl.db	C1:PSL-FSS_FASTGAIN	ao	FASTGAIN- fast loop gain adjust	VMIVME-4116	#C0 S7 @	Used everyday	TTFSS Interface (D04042 P1	11A		
61	c1psl	c1psl/psl.db	C1:PSL-FSS_SLOWDC	ao	SLOWDC- DC offset adjust of slow actuator	VMIVME-4116	#C1 S2 @	Most Useful	TTFSS Interface (D04042 P1	7A ?		
62	c1psl	c1psl/psl.db	C1:PSL-PMC_GAIN	ao	GAIN- overall pre-modecleaner servo loop gain	VMIVME-4116	#C1 S5 @	PMC	PMC SERVO (D980352)	P1	4A	
63	c1psl	c1psl/psl.db	C1:PSL-PMC_INOFFSET	ao	INOFFSET- input offset trim	VMIVME-4116	#C1 S6 @	PMC	PMC SERVO (D980352)	P1	5A	
64	c1psl	c1psl/psl.db	C1:PSL-PMC_PHCON	ao	PHCON- RF phase shifter control	VMIVME-4116	#C1 S7 @	PMC	Crystal Freq. Ref	P1	1A	
65	c1psl	c1psl/psl.db	C1:PSL-PMC_RFADJ	ao	RFADJ- RF output adjust	VMIVME-4116	#C2 S1 @	PMC	Crystal Freq. Ref	P1	3A	
66	c1psl	c1psl/psl.db	C1:PSL-PMC_RAMP	ao	RAMP- pre-modecleaner lock acquisition ramp	VMIVME-4116	#C2 S2 @	PMC	PMC SERVO (D980352)	P1	8A	
67	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_PIT_IN	ao	M1 Pit Mon	VMIVME-3113	#C0 S50 @	D980323 We can leave the Mode Cleaner Steering	NP1		1A	
68	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_YAW_IN	ao	M1 Yaw Mon	VMIVME-3113	#C0 S51 @	D980323 We can leave the Mode Cleaner Steering	NP1		2A	
69	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_PIT_IN	ao	M2 Pit Mon	VMIVME-3113	#C0 S52 @	D980323 We can leave the Mode Cleaner Steering	NP1		3A	
70	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_YAW_IN	ao	M2 Yaw Mon	VMIVME-3113	#C0 S53 @	D980323 We can leave the Mode Cleaner Steering	NP1		4A	
71	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_REFL_OFFSET	ao	MC RFPD bias voltage Adjust	VMIVME-4116	#C0 S4 @	The description is wrong. TProbably connected directly to the VME				
72	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_LIMIT	bi	MC_F limit reached	XVME-210	#C0 S15 @		MC Servo (D040180-B)	P2	9A	
73	c1psl	c1psl/psl.db	C1:PSL-FSS_SW1	bo	SW1- front panel switch 1	XVME-220	#C0 S4 @	TTFSS	TTFSS Interface (D04042 P1		1A	
74	c1psl	c1psl/psl.db	C1:PSL-FSS_SW2	bo	SW2- front panel switch 2	XVME-220	#C0 S5 @	TTFSS	TTFSS Interface (D04042 P1		2A	
75	c1psl	c1psl/psl.db	C1:PSL-FSS_FASTSWEEP	bo	FASTSWEEPTEST- front panel ramp input	XVME-220	#C0 S18 @	TTFSS	TTFSS Interface (D04042 P1		14A	
76	c1psl	c1psl/psl.db	C1:PSL-PMC_SW1	bo	SW1- switch 1	XVME-220	#C0 S9 @	PMC Servo	PMC SERVO (D980352)	P1	6A	
77	c1psl	c1psl/psl.db	C1:PSL-PMC_SW2	bo	SW2- switch 2	XVME-220	#C0 S10 @	PMC Servo	PMC SERVO (D980352)	P1	7A	
78	c1psl	c1psl/psl.db	C1:PSL-PMC_PHLIP	bo	PHLIP- flip phase by 180 degrees	XVME-220	#C0 S14 @	PMC Servo	Crystal Freq. Ref	P1	4A	
79	c1psl	c1psl/psl.db	C1:PSL-PMC_BLANK	bo	BLANK- blank input to PMC PZT	XVME-220	#C0 S15 @	PMC Servo	PMC SERVO (D980352)	P1	9A	
80	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG1_ATTEN	bo	SW1 demod1	XVME-220	#C0 S0 @	Not recorded. But still alive and effective. D980012	On WFS head			
81	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG2_ATTEN	bo	SW2 demod1	XVME-220	#C0 S1 @	Not recorded. But still alive and effective. D980012	On WFS head			
82	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG3_ATTEN	bo	SW3 demod1	XVME-220	#C0 S2 @	Not recorded. But still alive and effective. D980012	On WFS head			
83	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS1_SEG4_ATTEN	bo	SW4 demod1	XVME-220	#C0 S3 @	Not recorded. But still alive and effective. D980012	On WFS head			
84	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG1_ATTEN	bo	SW1 demod2	XVME-220	#C0 S8 @	Not recorded. But still alive and effective. D980012	On WFS head			
85	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG2_ATTEN	bo	SW2 demod2	XVME-220	#C0 S9 @	Not recorded. But still alive and effective. D980012	On WFS head			
86	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG3_ATTEN	bo	SW3 demod2	XVME-220	#C0 S10 @	Not recorded. But still alive and effective. D980012	On WFS head			
87	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-WFS2_SEG4_ATTEN	bo	SW4 demod2	XVME-220	#C0 S11 @	Not recorded. But still alive and effective. D980012	On WFS head			
88	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_PIT_POLARITY	bo	M1 Pit Invert	XVME-220	#C1 S0 @	D980323 We can leave the Mode Cleaner Steering	NP1		29A	
89	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM1_YAW_POLARITY	bo	M1 Yaw Invert	XVME-220	#C1 S1 @	D980323 We can leave the Mode Cleaner Steering	NP1		30A	
90	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_PIT_POLARITY	bo	M2 Pit Invert	XVME-220	#C1 S2 @	D980323 We can leave the Mode Cleaner Steering	NP1		31A	
91	c1ioo0	c1ioo0/ioo_wfs.db	C1:IOO-PZTM2_YAW_POLARITY	bo	M2 Yaw Invert	XVME-220	#C1 S3 @	D980323 We can leave the Mode Cleaner Steering	NP1		32A	
92	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_SW1	bo	Input Switch	XVME-220	#C2 S18 @		MC Servo (D040180-B)	P1	19A	
93	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_SW2	bo	AO Path Switch	XVME-220	#C2 S19 @		MC Servo (D040180-B)	P1	20A	
94	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_SW3	bo	INPUT 2 SWITCH	XVME-220	#C2 S20 @		MC Servo (D040180-B)	P1	21A	
95	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_BOOST1	bo	Comp Switch B2	XVME-220	#C2 S23 @		MC Servo (D040180-B)	P1	24A	
96	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_EXCA_EN	bo	Boost Excitation	XVME-220	#C2 S24 @		MC Servo (D040180-B)	P1	25A	
97	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_OPTIONA	bo	Daughter Board A Enable	XVME-220	#C2 S25 @		MC Servo (D040180-B)	P1	26A	
98	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_FILTER	bo	Filter Enable	XVME-220	#C2 S26 @		MC Servo (D040180-B)	P1	27A	
99	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_FASTSW	bo	Fast Path Enable	XVME-220	#C2 S27 @		MC Servo (D040180-B)	P1	28A	
100	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_POL	bo	Servo split polarity	XVME-220	#C2 S28 @		MC Servo (D040180-B)	P1	29A	
101	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_EXCB_EN	bo	B Excitation	XVME-220	#C2 S29 @		MC Servo (D040180-B)	P1	30A	
102	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_OPTIONB	bo	Daughter Board B Enable	XVME-220	#C2 S30 @		MC Servo (D040180-B)	P1	31A	
103	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_LIMITER	bo	Toggle Diodes	XVME-220	#C2 S31 @		MC Servo (D040180-B)	P1	32A	
104	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_LATCH_EN	bo	B Excitation	XVME-220	#C0 S16 @	Not sure what this is...	MC Servo (D040180-B)			
105	c1aux	c1aux/ShutterInterlo	C1:AUX-PSL_Shutter	bo	PSL mechanical shutter open/close	XVME-220	#C0 S10 @	to be moved to c1psl				
106	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_REFL_BITS	mbboDirect	REFL gain bits	XVME-220	#C2 S0 @	6bits	MC Servo (D040180-B)			
107	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_AO_BITS	mbboDirect	AO gain bits	XVME-220	#C2 S6 @	6bits	MC Servo (D040180-B)			
108	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_VCO_BITS	mbboDirect	VCO gain bits	XVME-220	#C2 S12 @	6bits	MC Servo (D040180-B)			
109	c1ioo0	c1ioo0/c1mc servo.d	C1:IOO-MC_BOOST2_BITS	mbboDirect	Boost bits	XVME-220	#C2 S21 @	2bits	MC Servo (D040180-B)	P1	22A,23A	