

Test

P1A	28	27	UL Coil Enable+	2	12	7	Digital Out	BIO1	0	C1:SUS-SRM_UL_ENABLE
P1C	28	27	UL Coil Enable-	2	31	26	Digital Out	BIO1	0	C1:SUS-SRM_UL_ENABLE
P1A	29	24	LL Coil Enable+	2	13	8	Digital Out	BIO1	1	C1:SUS-SRM_LL_ENABLE
P1C	29	24	LL Coil Enable-	2	32	27	Digital Out	BIO1	1	C1:SUS-SRM_LL_ENABLE
P1A	30	25	UR Coil Enable+	2	14	9	Digital Out	BIO1	2	C1:SUS-SRM_UR_ENABLE
P1C	30	25	UR Coil Enable-	2	33	28	Digital Out	BIO1	2	C1:SUS-SRM_UR_ENABLE
P1A	31	26	LR Coil Enable+	2	15	10	Digital Out	BIO1	3	C1:SUS-SRM_LR_ENABLE
P1C	31	26	LR Coil Enable-	2	34	29	Digital Out	BIO1	3	C1:SUS-SRM_LR_ENABLE
P1A	32	21	SD Coil Enable+	2	16	11	Digital Out	BIO2	4	C1:SUS-SRM_SD_ENABLE
P1C	32	21	SD Coil Enable-	2	35	30	Digital Out	BIO2	4	C1:SUS-SRM_SD_ENABLE