

Exc mon	Output	Amplitude	Stddev	Ratio				SEGMENT	Matrix					
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_I1_ERR	-9.67E-02	2.63E-02	2.72E-01										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_Q1_ERR	-4.14E-02	1.88E-02	4.53E-01	C1:SUS-MC1_ASCPIT_OUT	WFS1(I)	-9.67E-02	4.40E-02	WFS1(Q)	-4.14E-02	5.30E-02	1 2	1 1	-1.19E-01
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_I2_ERR	4.40E-02	2.41E-02	5.48E-01			1.23E-02	5.43E-02		-6.25E-03	-3.33E-02	4 3	-1 -1	
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_Q2_ERR	5.30E-02	6.53E-03	1.23E-01										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_I3_ERR	5.43E-02	2.45E-02	4.51E-01	WFS2(I)	-8.96E-01	7.96E-01	WFS2(Q)	-1.86E-01	2.73E-02	1 2	1 -1	-3.69E+00	
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_Q3_ERR	-3.33E-02	4.31E-03	1.29E-01			-9.73E-01	1.02E+00		-1.65E-01	5.03E-02	4 3	1 -1	
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_I4_ERR	1.23E-02	3.43E-02	2.78E+00										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS1_Q4_ERR	-6.25E-03	7.88E-03	1.26E+00										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_I1_ERR	-8.96E-01	5.65E-02	6.31E-02										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_Q1_ERR	-1.86E-01	1.35E-02	7.24E-02										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_I2_ERR	7.96E-01	4.53E-02	5.69E-02										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_Q2_ERR	2.73E-02	5.96E-03	2.19E-01										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_I3_ERR	1.02E+00	5.71E-02	5.57E-02										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_Q3_ERR	5.03E-02	5.93E-03	1.18E-01										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_I4_ERR	-9.73E-01	5.32E-02	5.46E-02										
CHA: C1:SUS-MC1_ASCPIT_OUT	CHB: C1:100-WFS2_Q4_ERR	-1.65E-01	1.42E-02	8.61E-02										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_I1_ERR	2.02E+00	7.55E-02	3.74E-02	C1:SUS-MC1_ASCYAW_OUT	WFS1(I)	2.02E+00	-1.93E+00	WFS1(Q)	1.08E+00	-1.96E-01	1 2	1 -1	8.19E+00
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_Q1_ERR	1.08E+00	4.26E-02	3.94E-02			2.20E+00	-2.04E+00		-4.07E-02	1.45E-02	4 3	-1 -1	
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_I2_ERR	-1.93E+00	7.00E-02	3.62E-02										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_Q2_ERR	-1.96E-01	1.40E-02	7.14E-02										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_I3_ERR	-2.04E+00	7.92E-02	3.89E-02	WFS2(I)	7.22E-02	2.07E-02	WFS2(Q)	2.18E-02	2.64E-03	1 2	1 1	1.74E-01	
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_Q3_ERR	1.45E-02	7.22E-03	4.89E-01			6.46E-03	-8.81E-02		-8.16E-03	2.41E-03	4 3	-1 -1	
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_I4_ERR	2.20E+00	8.01E-02	3.64E-02										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS1_Q4_ERR	-4.07E-02	1.30E-02	3.19E-01										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_I1_ERR	7.22E-02	4.10E-02	5.68E-01										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_Q1_ERR	2.18E-02	1.23E-02	5.64E-01										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_I2_ERR	2.07E-02	4.40E-02	2.13E+00										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_Q2_ERR	2.64E-03	6.51E-03	2.46E+00										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_I3_ERR	-8.81E-02	3.55E-02	4.03E-01										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_Q3_ERR	2.41E-03	5.73E-03	2.38E+00										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_I4_ERR	6.46E-03	5.53E-02	8.56E+00										
CHA: C1:SUS-MC1_ASCYAW_OUT	CHB: C1:100-WFS2_Q4_ERR	-8.16E-03	1.47E-02	1.80E+00										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_I1_ERR	2.14E+00	5.65E-02	2.64E-02	C1:SUS-MC2_ASCPIT_OUT	WFS1(I)	2.14E+00	2.46E+00	WFS1(Q)	1.16E+00	1.66E-01	1 2	1 1	8.85E+00
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_Q1_ERR	1.16E+00	3.45E-02	2.98E-02			-2.07E+00	-2.18E+00		1.20E-02	7.17E-02	4 3	-1 -1	
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_I2_ERR	2.46E+00	5.00E-02	2.03E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_Q2_ERR	1.66E-01	8.97E-03	5.42E-02	WFS2(I)	-1.70E+00	1.46E+00	WFS2(Q)	-3.55E-01	4.89E-02	1 2	1 -1	-6.86E+00	
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_I3_ERR	-2.18E+00	5.08E-02	2.33E-02			-1.77E+00	1.93E+00		-3.00E-01	9.38E-02	4 3	-1 -1	
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_Q3_ERR	7.17E-02	5.79E-03	8.07E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_I4_ERR	-2.07E+00	3.61E-02	1.74E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS1_Q4_ERR	1.20E-02	1.08E-02	9.05E-01										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_I1_ERR	-1.70E+00	7.10E-02	4.18E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_Q1_ERR	-3.55E-01	1.80E-02	5.06E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_I2_ERR	1.46E+00	5.80E-02	3.98E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_Q2_ERR	4.89E-02	8.42E-03	1.72E-01										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_I3_ERR	1.93E+00	6.60E-02	3.42E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_Q3_ERR	9.38E-02	6.08E-03	6.48E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_I4_ERR	-1.77E+00	4.77E-02	2.70E-02										
CHA: C1:SUS-MC2_ASCPIT_OUT	CHB: C1:100-WFS2_Q4_ERR	-3.00E-01	1.32E-02	4.40E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_I1_ERR	-2.03E+00	4.81E-02	2.37E-02	C1:SUS-MC2_ASCYAW_OUT	WFS1(I)	-2.03E+00	2.28E+00	WFS1(Q)	-1.08E+00	1.35E-01	1 2	1 -1	-8.82E+00
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_Q1_ERR	-1.08E+00	2.63E-02	2.43E-02			-2.54E+00	1.97E+00		1.09E-01	-6.86E-02	4 3	-1 -1	
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_I2_ERR	2.28E+00	6.62E-02	2.90E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_Q2_ERR	1.35E-01	1.04E-02	7.71E-02	WFS2(I)	4.47E+00	4.63E+00	WFS2(Q)	1.22E+00	1.20E-01	1 2	1 1	1.79E+01	
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_I3_ERR	1.97E+00	4.65E-02	2.36E-02			-5.96E+00	-2.83E+00		-1.25E+00	-6.48E-02	4 3	-1 -1	
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_Q3_ERR	-6.86E-02	1.12E-02	1.64E-01										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_I4_ERR	-2.54E+00	6.84E-02	2.69E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS1_Q4_ERR	1.09E-01	1.31E-02	1.20E-01										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_I1_ERR	4.47E+00	1.08E-01	2.41E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_Q1_ERR	1.22E+00	2.94E-02	2.42E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_I2_ERR	4.63E+00	9.22E-02	1.99E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_Q2_ERR	1.20E-01	8.15E-03	6.79E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_I3_ERR	-2.83E+00	7.75E-02	2.74E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_Q3_ERR	-6.48E-02	7.57E-03	1.17E-01										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_I4_ERR	-5.96E+00	1.17E-01	1.96E-02										
CHA: C1:SUS-MC2_ASCYAW_OUT	CHB: C1:100-WFS2_Q4_ERR	-1.25E+00	2.71E-02	2.16E-02										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_I1_ERR	6.89E-01	3.25E-02	4.72E-02	C1:SUS-MC3_ASCPIT_OUT	WFS1(I)	6.89E-01	9.20E-01	WFS1(Q)	3.56E-01	4.31E-02	1 2	1 1	3.07E+00
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_Q1_ERR	3.56E-01	1.76E-02	4.94E-02			-7.17E-01	-7.40E-01		1.57E-02	3.44E-02	4 3	-1 -1	
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_I2_ERR	9.20E-01	3.93E-02	4.27E-02										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_Q2_ERR	4.31E-02	5.95E-03	1.38E-01	WFS2(I)	5.60E-01	-4.96E-01	WFS2(Q)	1.39E-01	-4.66E-02	1 2	1 -1	1.72E+00	
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_I3_ERR	-7.40E-01	3.07E-02	4.15E-02			3.12E-01	-3.56E-01		3.36E-02	-9.89E-03	4 3	-1 -1	
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_Q3_ERR	3.44E-02	5.86E-03	1.70E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_I4_ERR	-7.17E-01	3.98E-02	5.54E-02										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS1_Q4_ERR	1.57E-02	9.31E-03	5.92E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_I1_ERR	5.60E-01	4.98E-02	8.90E-02										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_Q1_ERR	1.39E-01	1.35E-02	9.71E-02										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_I2_ERR	-4.96E-01	5.21E-02	1.05E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_Q2_ERR	-4.66E-02	7.98E-03	1.71E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_I3_ERR	-3.56E-01	4.43E-02	1.24E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_Q3_ERR	-9.89E-03	6.47E-03	6.54E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_I4_ERR	3.12E-01	4.67E-02	1.50E-01										
CHA: C1:SUS-MC3_ASCPIT_OUT	CHB: C1:100-WFS2_Q4_ERR	3.36E-02	1.31E-02	3.01E-01										
CHA: C1:SUS-MC3_ASCYAW_OUT	CHB: C1:100-WFS1_I1_ERR	-1.44E+00												