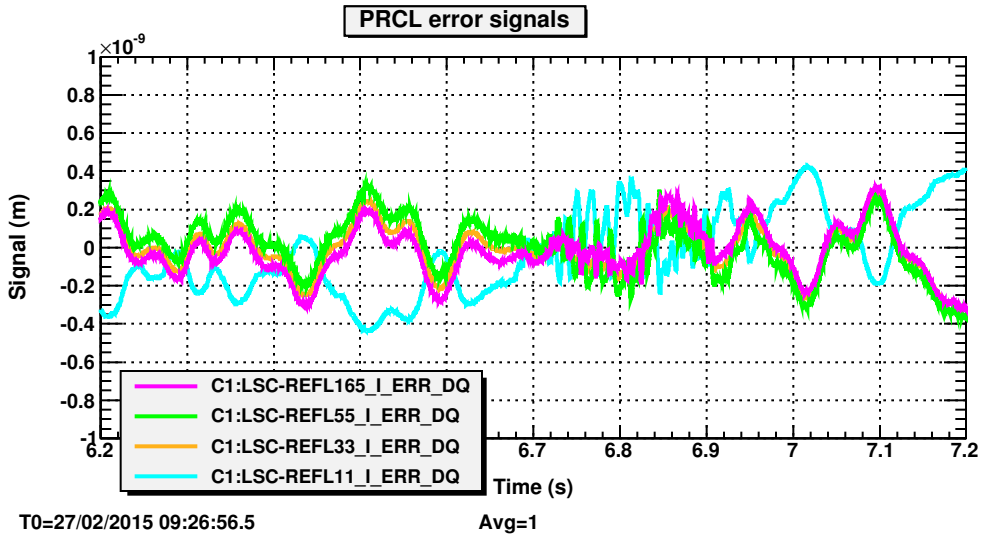
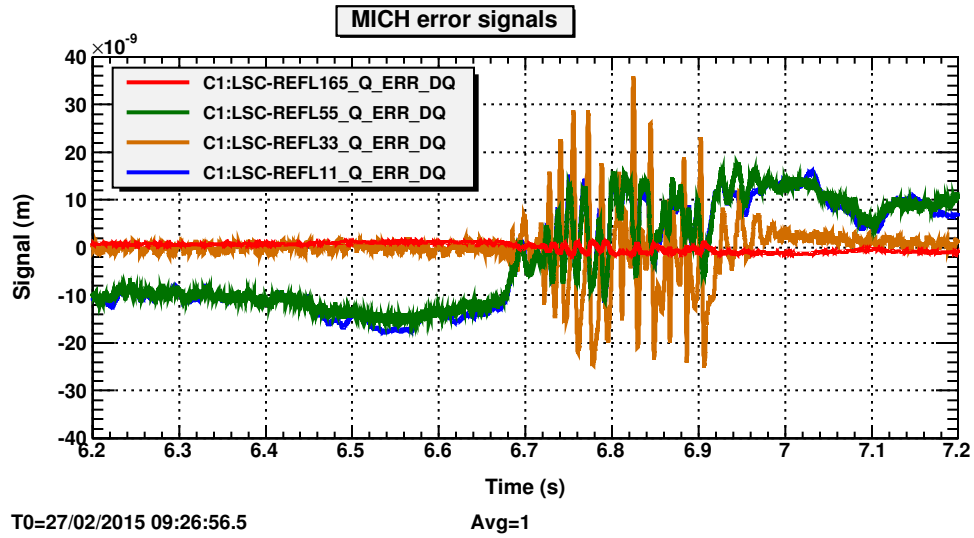
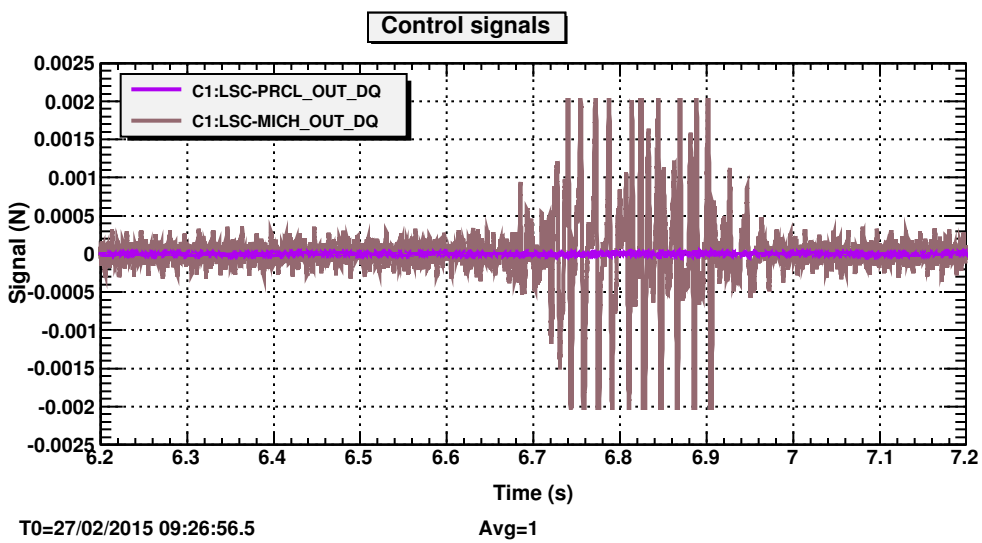
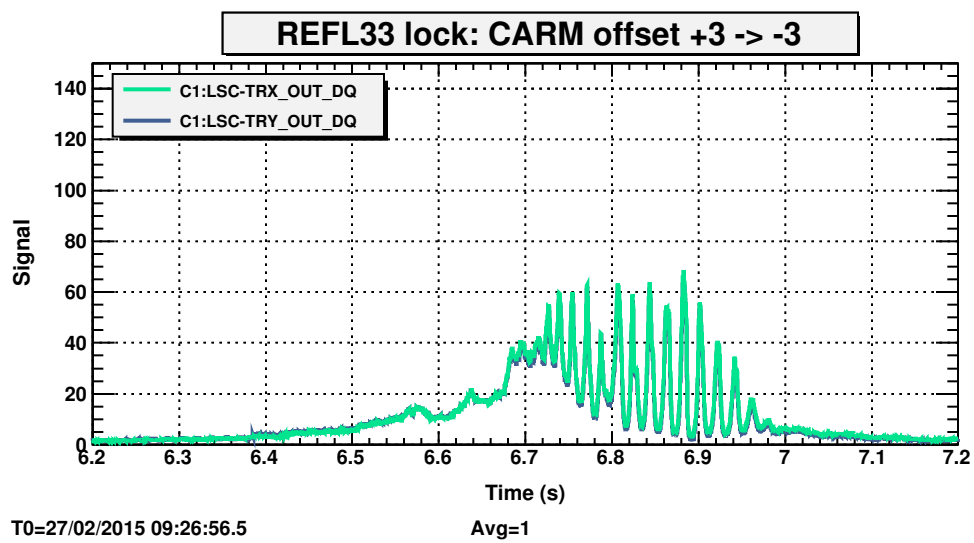

PRCL error signals
×10^-9
Signal (m)
Time (s)
C1:LSC-REFL165_I_ERR_DQ
C1:LSC-REFL55_I_ERR_DQ
C1:LSC-REFL33_I_ERR_DQ
C1:LSC-REFL11_I_ERR_DQ
T0=27/02/2015 09:26:56.5
Avg=1


MICH error signals
×10^-9
Signal (m)
Time (s)
C1:LSC-REFL165_Q_ERR_DQ
C1:LSC-REFL55_Q_ERR_DQ
C1:LSC-REFL33_Q_ERR_DQ
C1:LSC-REFL11_Q_ERR_DQ
T0=27/02/2015 09:26:56.5
Avg=1


Control signals
Signal (N)
Time (s)
C1:LSC-PRCL_OUT_DQ
C1:LSC-MICH_OUT_DQ
T0=27/02/2015 09:26:56.5
Avg=1


REFL33 lock: CARM offset +3 -> -3
Signal
Time (s)
C1:LSC-TRX_OUT_DQ
C1:LSC-TRY_OUT_DQ
T0=27/02/2015 09:26:56.5
Avg=1