

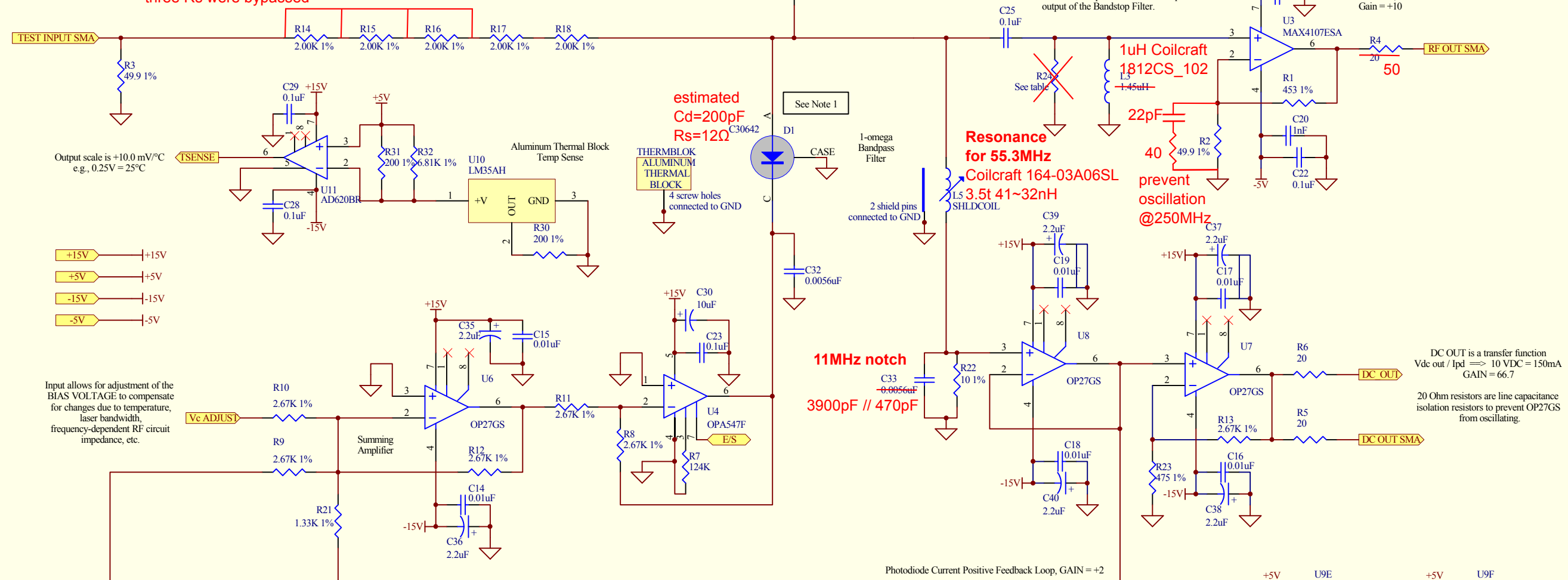
CIRCUITRY ON THIS PAGE IS WITHIN THE RF CAGE

L&C of TEST INPUT produces a notch@~60MHz three Rs were bypassed

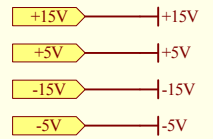
Multiple resistors divide out the distributed capacitance between TEST INPUT and RF CIRCUIT

100nH Coilcraft 1206CS_101
110MHz notch
3.5-50pF nominal 20.5pF

R24 may not be installed. In the previous revision 100pF has been installed on the R3 pad in order to achieve 81/162 MHz operation.
 At lower frequencies a resistor may be installed to impedance match the input and output of the Bandstop Filter.



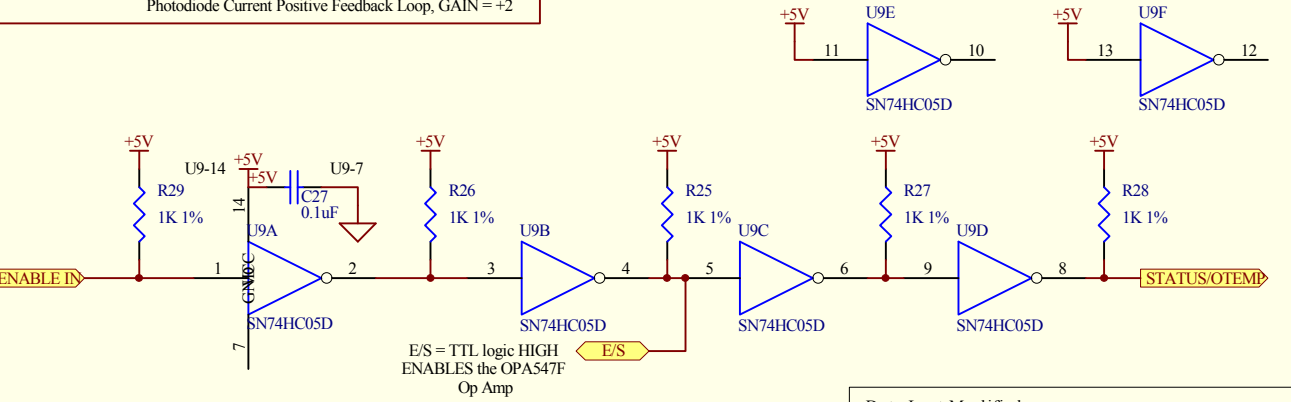
Output scale is +10.0 mV/°C
 e.g., 0.25V = 25°C



Input allows for adjustment of the BIAS VOLTAGE to compensate for changes due to temperature, laser bandwidth, frequency-dependent RF circuit impedance, etc.

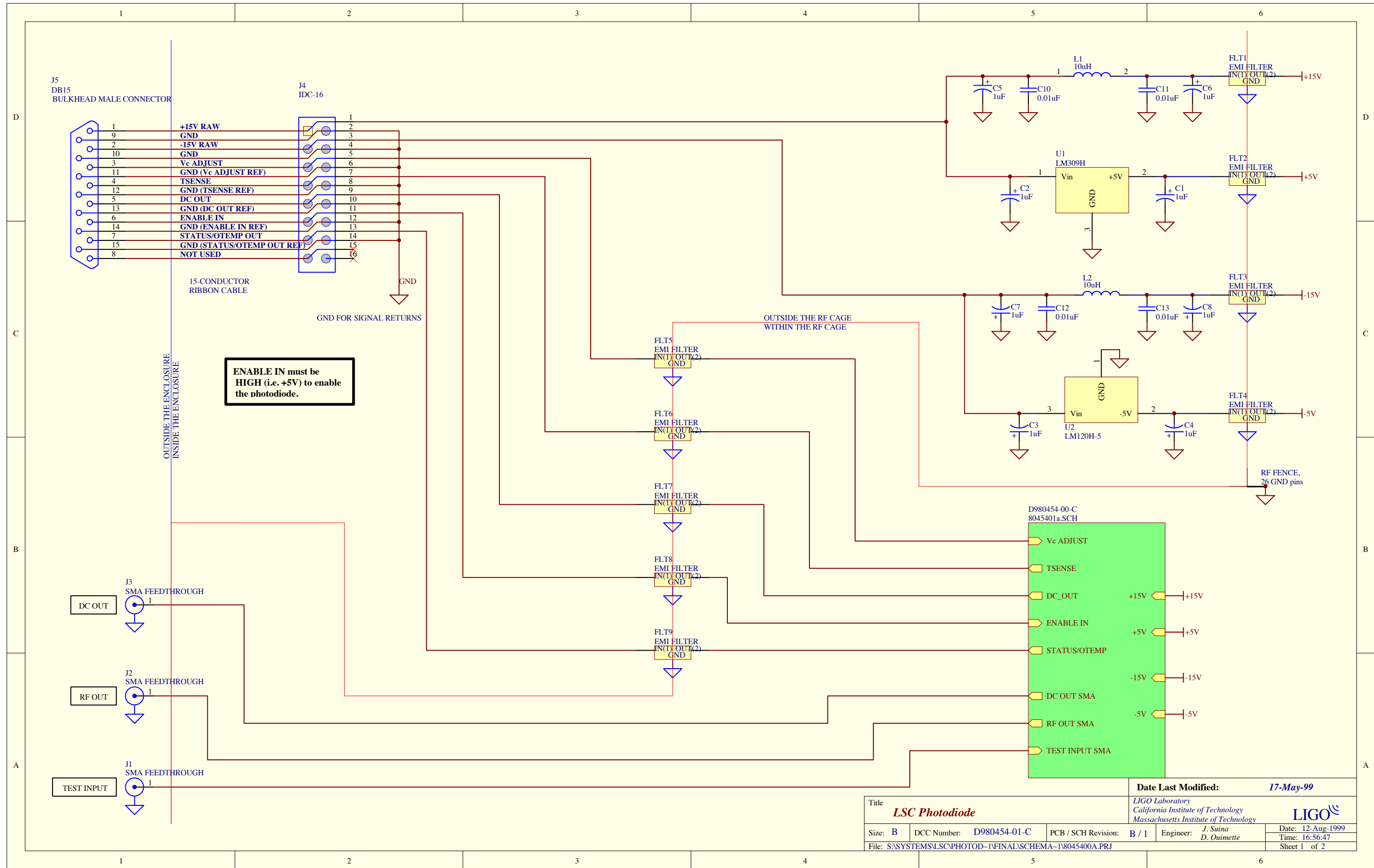
POP55 modification by K. Arai May 20, 2011

- NOTES:
- 1) The same TO-5 footprint accommodates the following diodes: C30642, GAP2000
 - 2) See table on page 3 for frequencies and values of L4, L5, R24.



DC OUT is a transfer function
 $V_{dc\ out} / I_{pd} \Rightarrow 10\ VDC = 150mA$
 GAIN = 66.7
 20 Ohm resistors are line capacitance isolation resistors to prevent OP27GS from oscillating.

Title		Date Last Modified: 17-May-99	
LSC Photodiode		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology	
Size: B	DCC Number: D980454-01-C	PCB / SCH Revision: B / 1	Engineer: J. Suina D. Ouimette
File: S:\SYSTEMS\LSCPHOTOD-1\FINAL\SCHEMA-1\8045401A.SCH		Date: 12-Aug-1999	Time: 16:56:25
		Sheet 2 of 2	



ENABLE IN must be HIGH (i.e. +5V) to enable the photodiode.

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File: S:\SYSTEMS\LSCPHOTOD-1\FINAL\SCHEMA-1\8045400A.PRJ		Date: 12-Aug-1999	Time: 16:56:47
		Sheet 1 of 2	