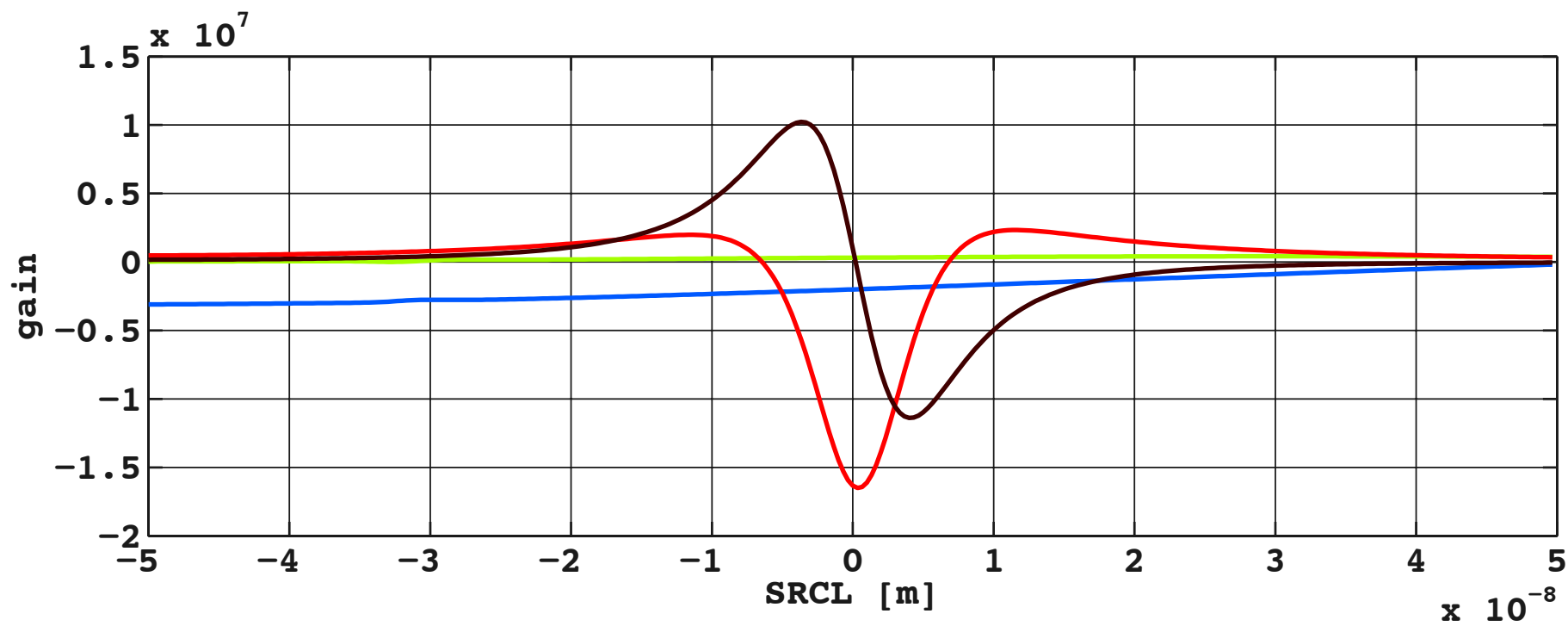
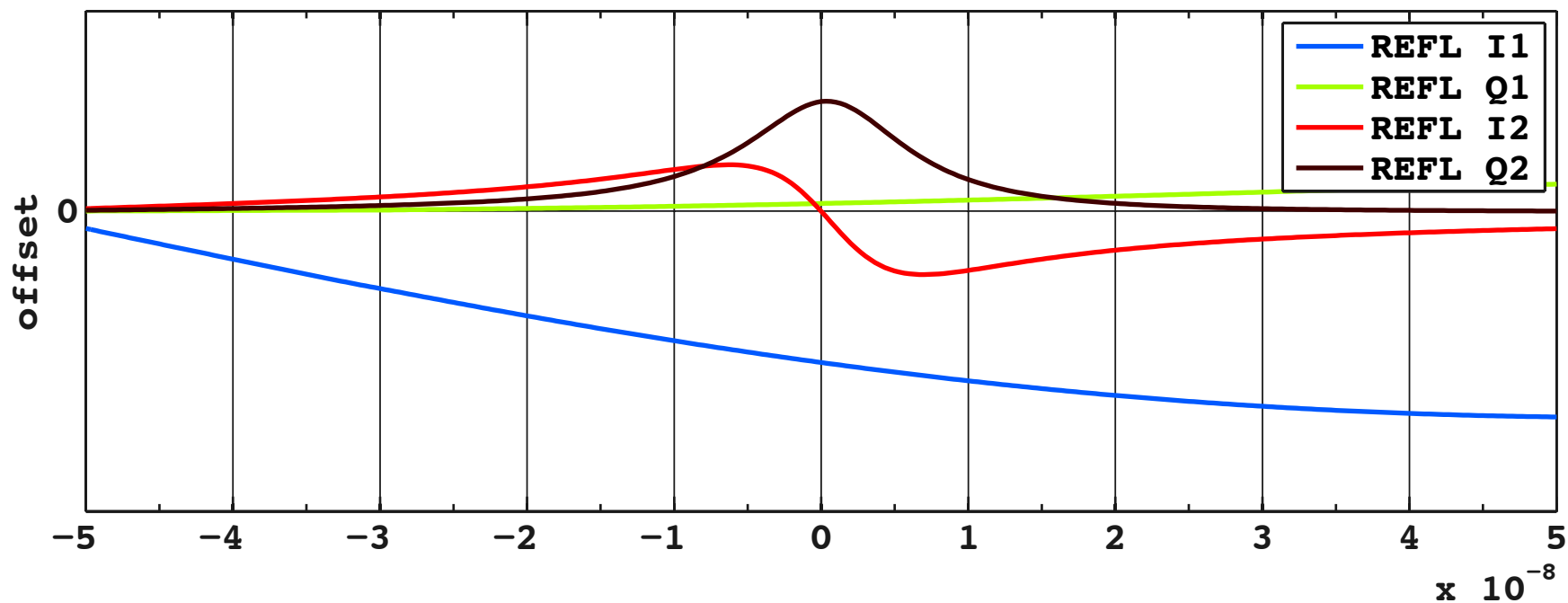
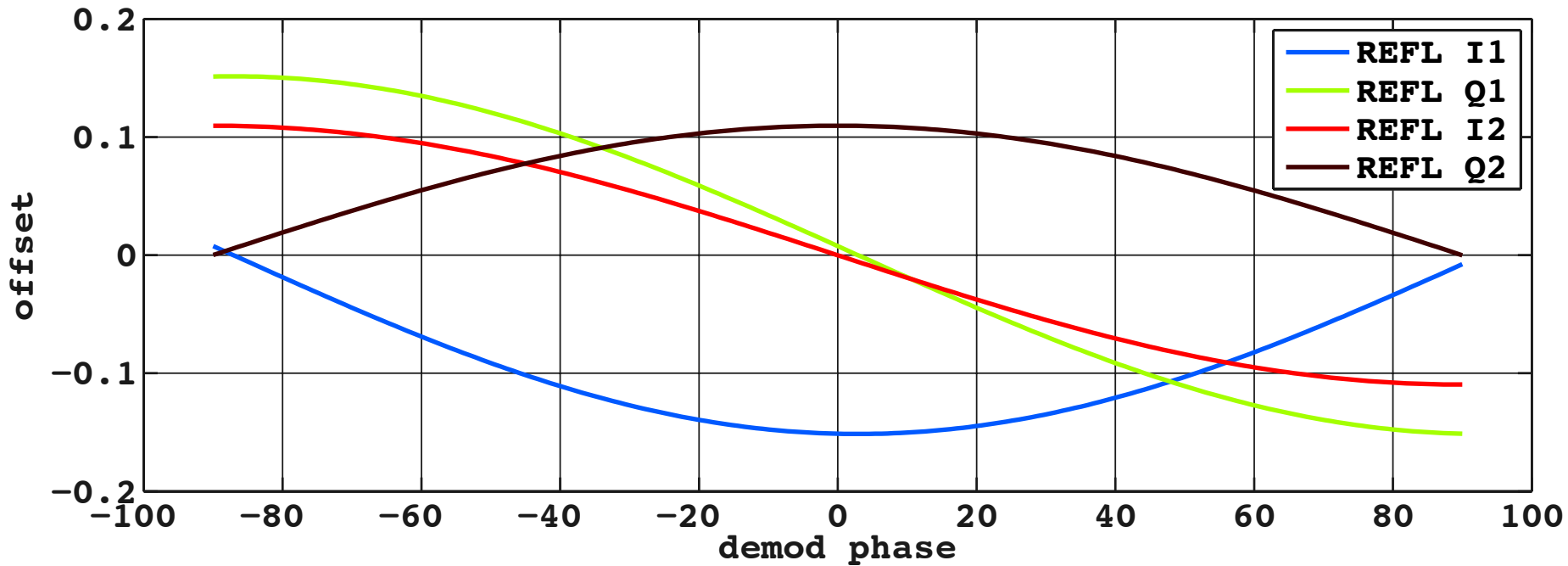


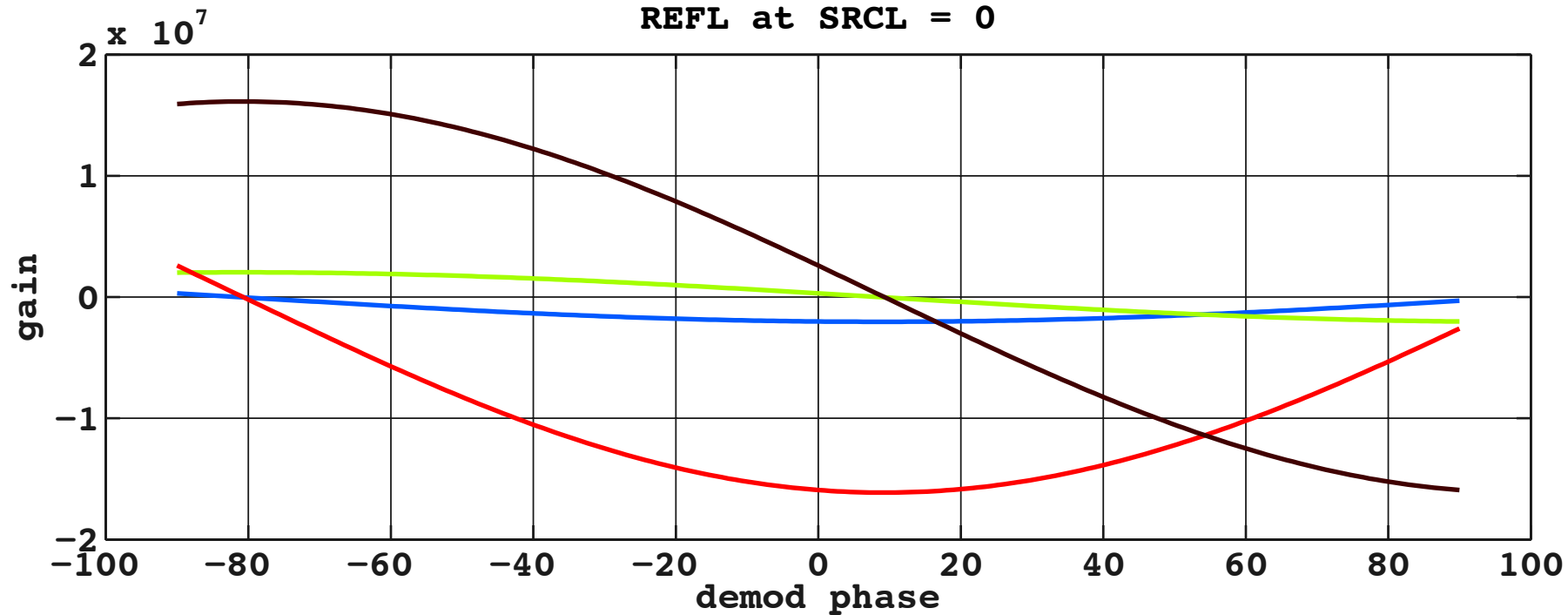
REFL



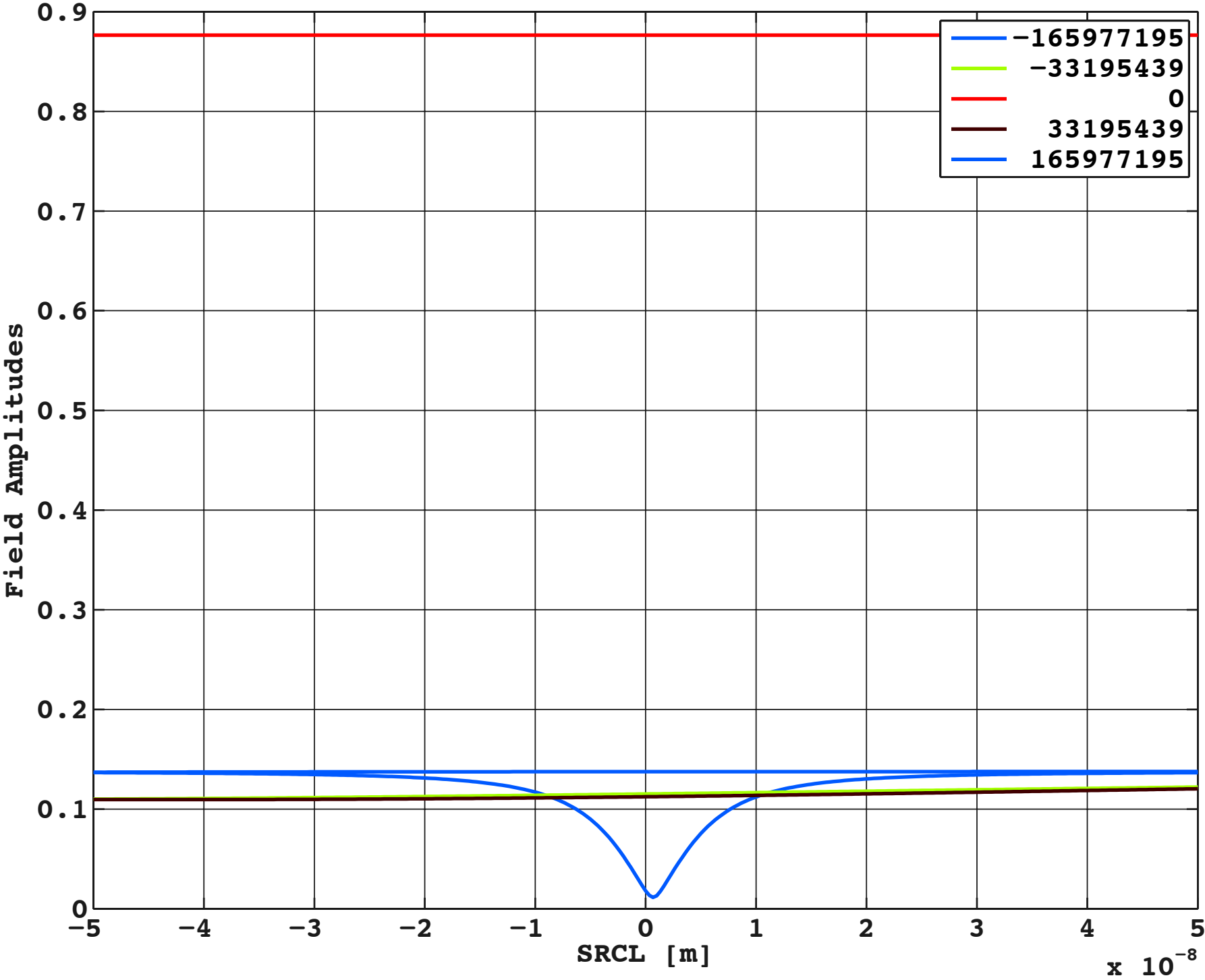
REFL at SRCL = 0



REFL at SRCL = 0



RF fields at REFL



Varying the demod phase

