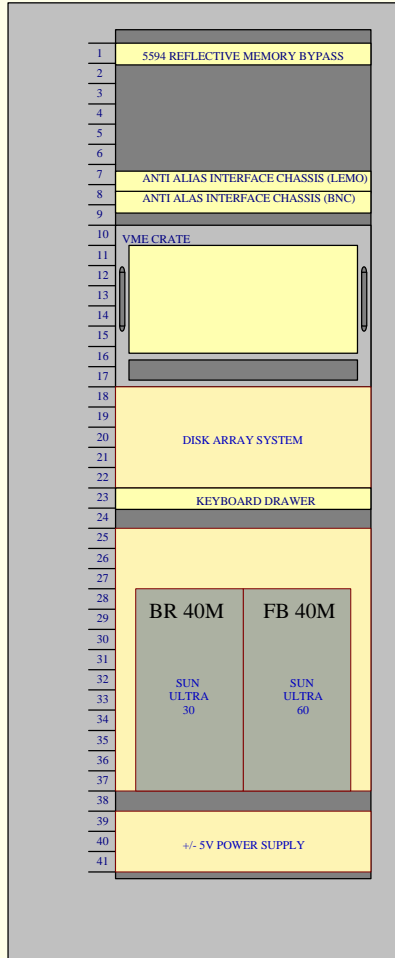


1Y7



If one node is removed, this bypass ensures that the reflective memory loop is uninterrupted

Contains anti-aliasing filters with two connector types, BNC and LEMO.

Crate contains the data aquisition pentium, an EDCU and an ADCU.

500GB RAID disk array written to by the Ultra 60.

Keyboard drawer.

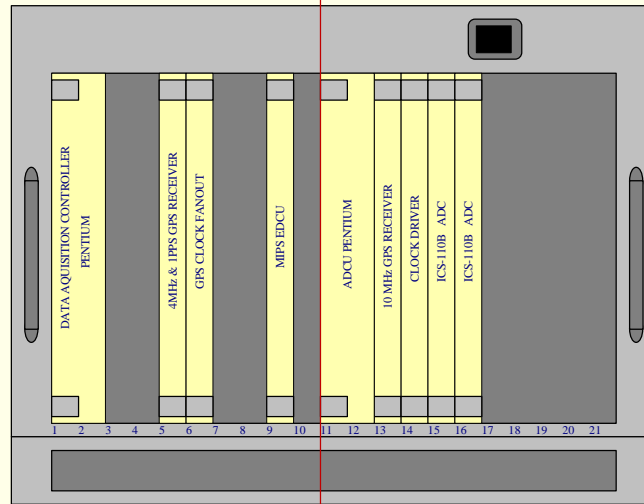
Sun Ultra 60: Frame builder, writes to the RAID array, can be queried from the dataviewer. Not an epics server.

Sun Ultra 30: Frame broadcaster, broadcasts frames to whomever needs them.

+/- 5V power supplies for the anti-aliasing filters.

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1Y7-10



Data Acquisition Pentium: Receives data from ADCU's, puts it into useable packets and sends it to the Ultra 60.

MIPS EDCU: epics DCU and epics interface for the DAQ.

4MHz & 1pps GPS reciever: custom reciever that transmits a 4MHz output clock timing signal, that is phase locked to a 1pps trigger signal which synchronizes the system.

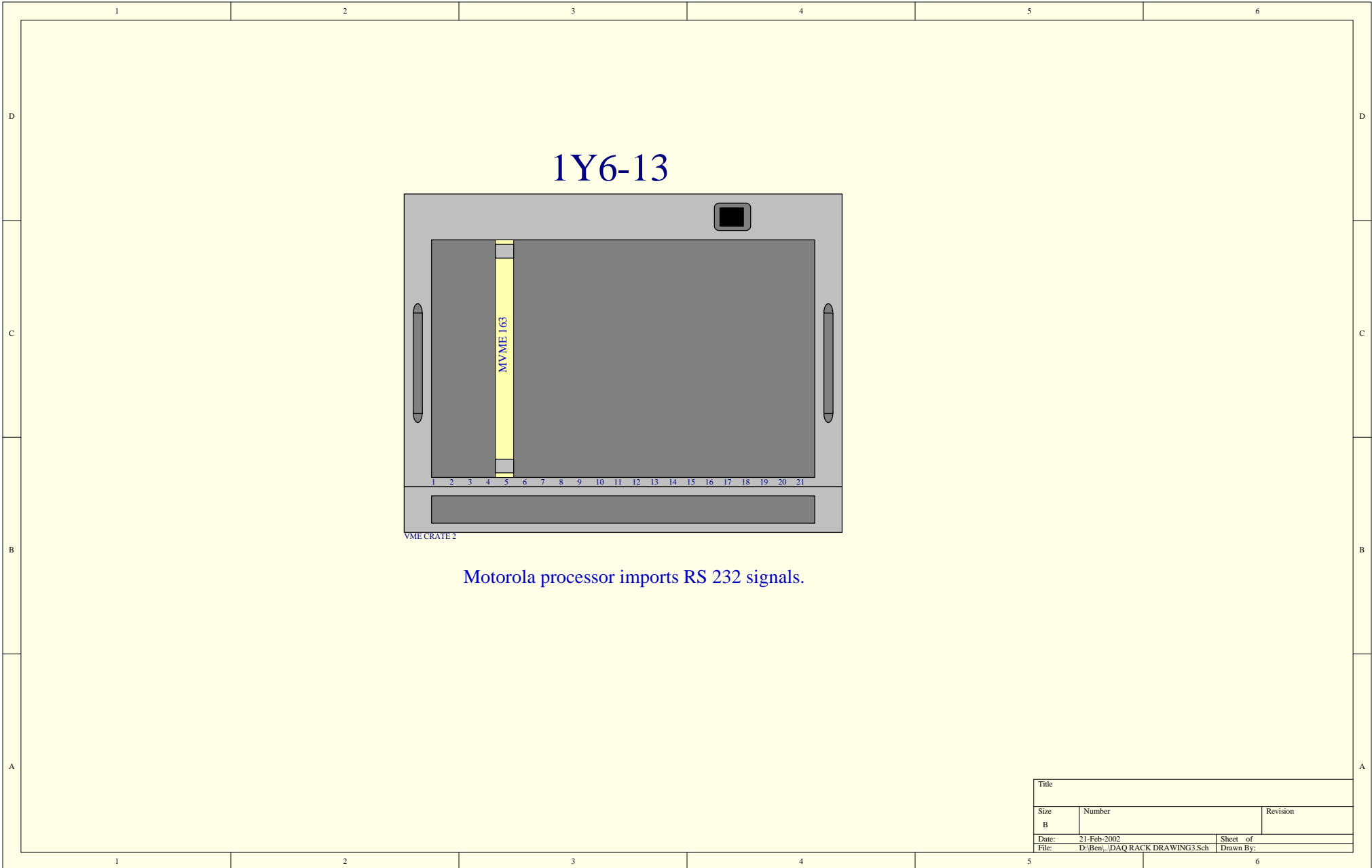
GPS clock fanout: recieves 2 BNCs from the GPS receiver and converts them to an set of eight 4-pin LEMO connectors that go to the clock drivers in the ADCUS

ADCU Pentium: aquires data, time stamps it, and puts it into reflective memory.

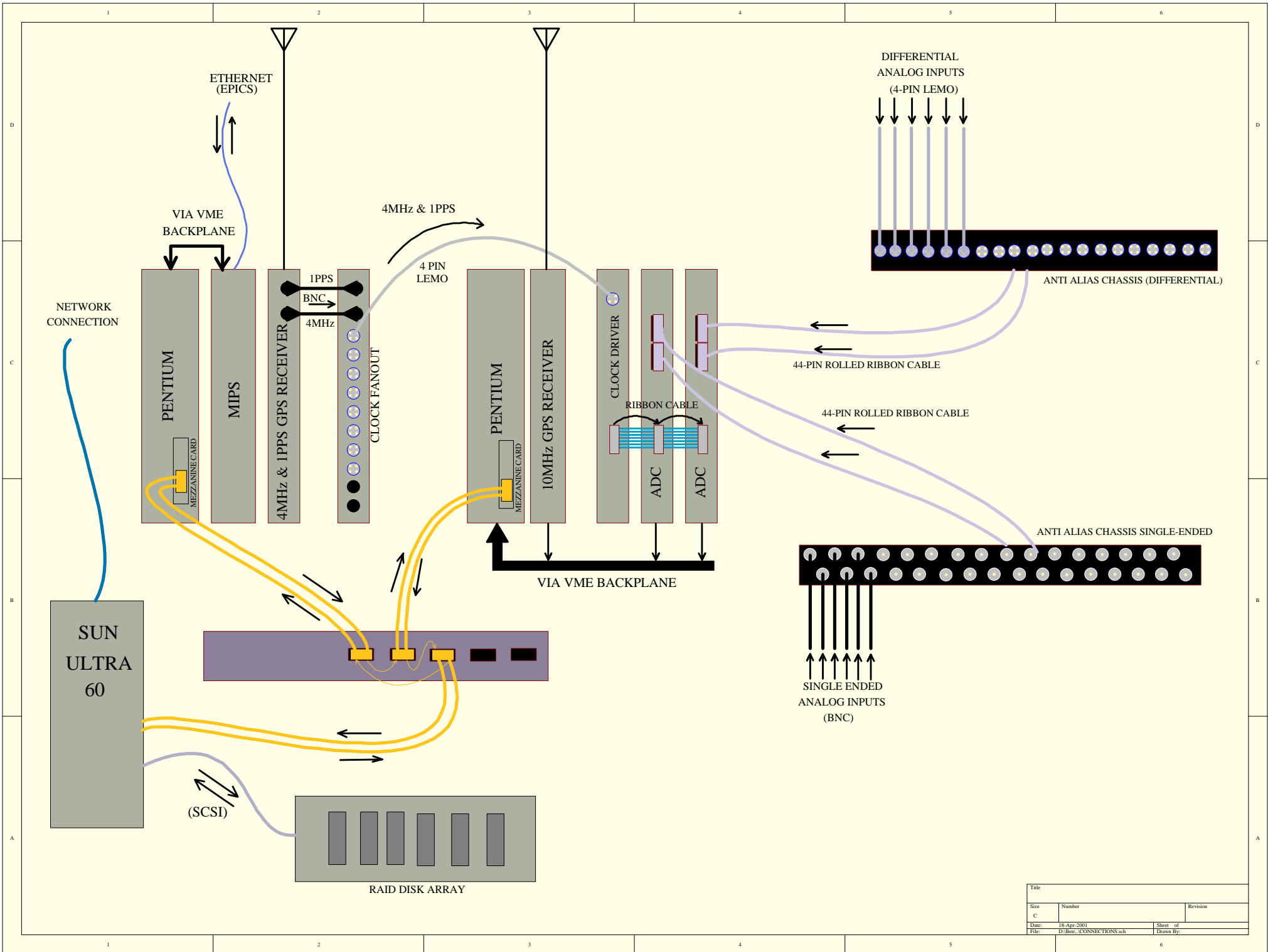
GPS receiver: A 10MHz GPS clock signal for the pentium time stamping.

Clockdriver: ICS clock driver for ADC timing. Recieves both 1pps trigger, and 4MHz from the GPS Clock Fanout. Outputs proper timing pulses to the ICS 110B ADCs

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